

What is claimed is:

1. A drive circuit comprising:
an input node for receiving data;
5 an output node:
a first MOS transistor of a first conductivity type,
the first MOS transistor having a source, a drain connected to the output node, and a
gate connected to the input node;
a second MOS transistor of the first conductivity type, the second MOS
10 transistor having a source, a drain connected to the source of the first MOS transistor,
and a gate supplied with a predetermined potential level; and
resistance means connected between the source of the second MOS transistor
and a source node supplied with a source potential level.
- 15 2. The drive circuit according to claim 1, wherein the resistance means
comprises a MOS transistor.
3. The drive circuit according to claim 1, wherein the second MOS transistor
supplies a constant current to the output node.
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4. The drive circuit according to claim 1, further including a third MOS transistor
of a second conductivity type, the third MOS transistor having a source connected to a
ground node supplied with a ground potential level, a drain connected to the output
node, and a gate connected to the input node.
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5. A drive circuit comprising:
an input node for receiving data;

an output node;

a first MOS transistor of a first conductivity type, the first MOS transistor having a source, a drain connected to the output node, and a gate connected to the input node;

a second MOS transistor of the first conductivity type, the second MOS

5 transistor having a source, a drain connected to the source of the first MOS transistor, and a gate supplied with a predetermined potential level; and

resistance means connected between the source of the second MOS transistor and a ground node supplied with a ground potential level.

10 6. The drive circuit according to claim 5, wherein the resistance means comprises a MOS transistor.

7. The drive circuit according to claim 5, further including a third MOS transistor of a second conductivity type, the third MOS transistor having a source connected to a
15 source node supplied with a source potential level, a drain connected to the output node, and a gate connected to the input node.

8. The drive circuit according to claim 5, wherein the second MOS transistor supplies a constant current to the output node.

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9. A drive circuit comprising:

an input node for receiving data;

an output node;

a first MOS transistor of a first conductivity type, the first MOS transistor having a

25 source, a drain connected to the output node, and a gate supplied with a predetermined potential level; and

a second MOS transistor of the first conductivity type, the second MOS

transistor having a source connected to a source node supplied with a source potential level, a drain connected to the source of the first MOS transistor, and a gate connected to the input node.

5 10. The drive circuit according to claim 9, further including a third MOS transistor of a second conductive type, the third MOS transistor having a source connected to a ground node supplied with a ground potential level, a drain connected to the output node, and a gate connected to the input node.

10 11. The drive circuit according to claim 9, wherein the first MOS transistor supplies a constant current to the output node.

 12. A drive circuit comprising:
 an input node for receiving data;
15 an output node
 a first MOS transistor of a first conductivity type, the first MOS transistor having a source, a drain connected to the output node, and a gate supplied with a predetermined potential level; and
 a second MOS transistor of the first conductivity type, the second MOS
20 transistor having a source connected to a ground node supplied with a ground potential level, a drain connected to the source of the first MOS transistor, and a gate connected to the input node.

 13. The drive circuit according to claim 12, further including a third MOS
25 transistor of a second conductive type, the third MOS transistor having a source connected to a source node supplied with a source potential level, a drain connected to the output node, and a gate connected to the input node.

14. The drive circuit according to claim 12, wherein the first MOS transistor supplies a constant current to the output node.

- 5 15. A drive circuit comprising
a source node supplied with a source potential level;
a ground node supplied with a ground potential level;
a data input node for receiving data;
an output node to which a light-emitting device is connected;
10 a first MOS transistor of a first conductivity type, the first MOS transistor having a source, a drain connected to the output node, and a gate connected to the data input node;
a second MOS transistor of a second conductivity type, the second MOS transistor having a source connected to the ground node, a drain connected to the
15 output node, and a gate connected to the data input node;
a third MOS transistor of the first conductivity type, the third MOS transistor having a source, a drain connected to the source of the first MOS transistor, and a gate supplied with a predetermined potential level between the source potential level and the ground potential level; and
20 resistance means connected between the source node and the source of the third MOS transistor.

16. The drive circuit according to claim 15, wherein the resistance means comprises a MOS transistor.

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17. The drive circuit according to claim 15, wherein the third MOS transistor supplies a constant current to the output node.

18. A drive circuit comprising:

a source node supplied with a source potential level;

a ground node supplied with a ground potential level;

5 a data input node for receiving data;

an output node connected to which a light-emitting device is connected;

a first MOS transistor of a first conductivity type, the first MOS transistor having a source, a drain connected to the output node, and a gate supplied with a predetermined potential level between the source potential level and the ground

10 potential level;

a second MOS transistor of the first conductive type, the second MOS transistor having a source connected to the source node, a drain connected to the source of the first MOS transistor, and a gate connected to the data input node; and

15 a third MOS transistor of a second conductive type, the third MOS transistor having a source connected to the ground node, a drain connected to the output node, and a gate connected to the data input node.

19. The drive circuit according to claim 18, wherein the first MOS transistor supplies a constant current to the output node.

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